

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage at First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: May 27, 2004

Signature: Paul B. Stephens

Paul B. Stephens



Docket No.: 30320/17232  
(PATENT)

IFW

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Levit-Gurevich et al.

Application No.: 10/692,946

Group Art Unit: 2123

Filed: October 24, 2003

Examiner: To be assigned

For: Ultra Fast Multi-Processor System Simulation  
Using Dedicated Virtual Machines

**INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned.

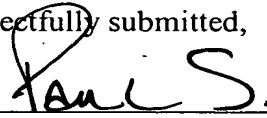
A copy of each reference listed on the PTO-1449 is attached.

The Commissioner is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith to our Deposit Account No. 13-2855, under Order No. 30320/17232. A duplicate copy of this paper is enclosed.

Dated: May 27, 2004

Respectfully submitted,

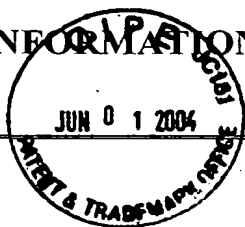
By



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Form PTO-1449 (Modified)	Atty. Docket No. 30320/17232	Serial No. 10/692,946
<b>INFORMATION DISCLOSURE STATEMENT</b>	Applicant(s) Levit-Gurevich et al.	
	Filing Date October 24, 2003	Art Unit 2123

**U.S. PATENT DOCUMENTS**

Examiner Initials	Document Number	Issue or Publication Date	Name	Class	Subclass	Filing Date (If Appropriate)

**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Document Number	Publication Date	Country	Translation Yes      No	

**OTHER DOCUMENTS**

	AMD Releases x86-64TM Technology Simulator to Allow Developers to Test and Perfect 64-Bit Code; 3 pages [Online] (Projected publication October, 2000)
	AMD Showcases First "VirtuHammer" Simulator At LinuxWorld Shows In New York and Paris; 4 pages [Online] (Projected publication January, 2001)
	Press Release: Virtutech Previews Simics 2.0: Simulating Over 500 Million Instructions Per Second!; 3 pages [Online] (Projected publication April, 2003)
	Uhlig, et al. "SoftSDV: A Presilicon Software Development Environment for the IA-64 Architecture," Intel Technology Journal, pages 1-14 (1999)

EXAMINER:	DATE CONSIDERED:
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